

Fairchild

Diode FJT1101

Datasheet

Silicon – Diode

FJT1101

20V/150mA

DATASHEET

OEM – Fairchild

Source: Fairchild Databook 1978

FJT1100•FJT1101**ULTRA LOW LEAKAGE
DIFFUSED SILICON PLANAR DIODES**

- * $I_R \dots 1.0 \text{ pA (MAX) @ } 5 \text{ V (FJT1100)}$
- * $BV \dots 20 \text{ V (MIN) (FJT1100)}$

ABSOLUTE MAXIMUM RATINGS (Note 1)**Temperature**

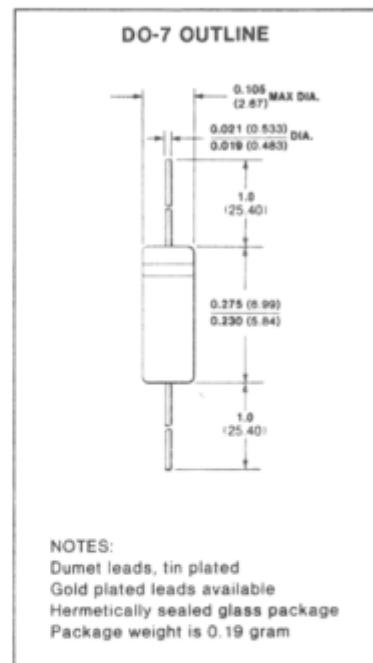
Storage Temperature Range	-55°C to +200°C
Maximum Junction Operating Temperature	+175°C
Lead Temperature	+260°C

Power Dissipation (Note 2)

Maximum Total Power Dissipation at 25°C Ambient	250 mW
Linear Power Derating factor (from 25°C)	1.67 mW / °C

Maximum Voltage and Current

WIV	Working Inverse Voltage	FJT1100	25 V
I_F	Continuous Forward Current	FJT1101	15 V 150 mA

**ELECTRICAL CHARACTERISTICS (25°C Ambient Temperature unless otherwise noted)**

SYMBOL	CHARACTERISTIC		MIN	MAX	UNITS	TEST CONDITIONS
BV	Breakdown Voltage	FJT1100 FJT1101	30 20		V V	$I_R = 5.0 \mu\text{A}$ $I_R = 5.0 \mu\text{A}$
I_R	Reverse Current	FJT1100 FJT1101		1.0 10 5.0 15	pA pA pA pA	$V_R = 5.0 \text{ V}$ $V_R = 15 \text{ V}$ $V_R = 5.0 \text{ V}$ $V_R = 15 \text{ V}$
V_F	Forward Voltage	FJT1100 FJT1101		1.05 1.10	V V	$I_F = 50 \text{ mA}$ $I_F = 50 \text{ mA}$
C	Capacitance	FJT1100 FJT1101		1.5 1.8	pF pF	$V_R = 0, f = 1 \text{ MHz}$ $V_R = 0, f = 1 \text{ MHz}$

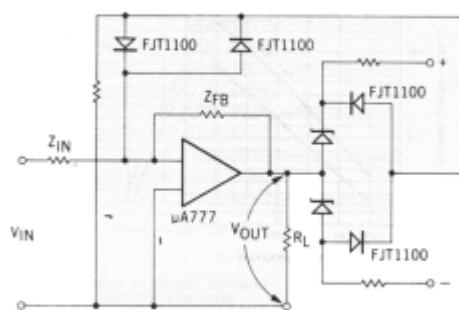
NOTES:

1. These are limiting values above which the serviceability of the diode may be impaired.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty-cycle operation.
3. For product family characteristic curves and applications information, refer to Chapter 4, D6.

CURVE SET NUMBER D6

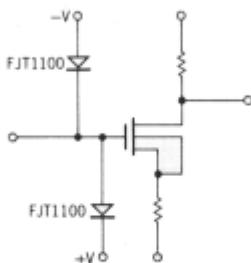
ULTRA-LOW LEAKAGE SMALL SIGNAL DIODE

A BOUND CIRCUIT FOR OPERATIONAL AMPLIFIERS



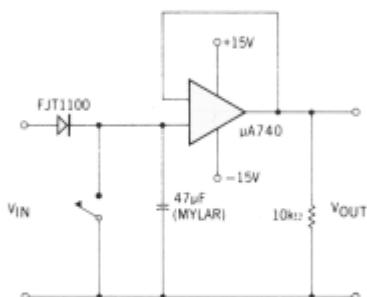
The bound circuit prevents overloading and saturation of operational amplifiers. The circuit has negligible effect on the operational amplifier until overload conditions occur. The use of the low leakage picocoulomb diode permits realization of extremely high input impedance for normal input voltages.

MOS FET PROTECTION CIRCUIT



The picocoulomb diode affords excellent gate voltage protection while maintaining the DC input impedance at about one million megohms. In addition the very low capacity of the FJT1100 will have a relatively small effect on the circuit input capacity.

PEAK FOLLOWER CIRCUIT



A nearly constant voltage peak follower circuit is available by using a picocoulomb diode. A comparison between the use of the FJT1100 and a "low leakage" FDH333 diode in the circuit is shown in the curves of V_{OUT} vs Time.

